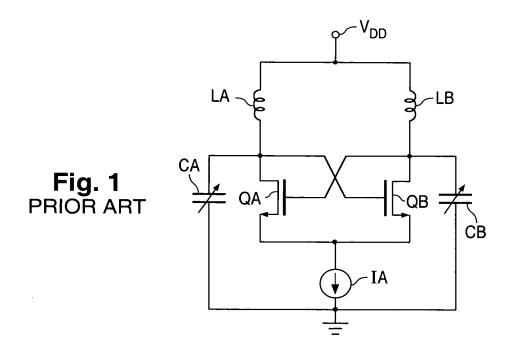
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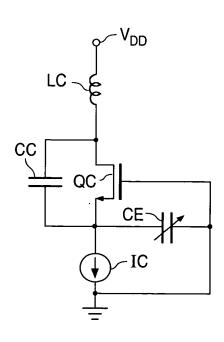


Fig. 2a PRIOR ART

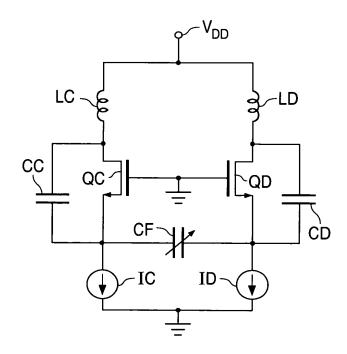
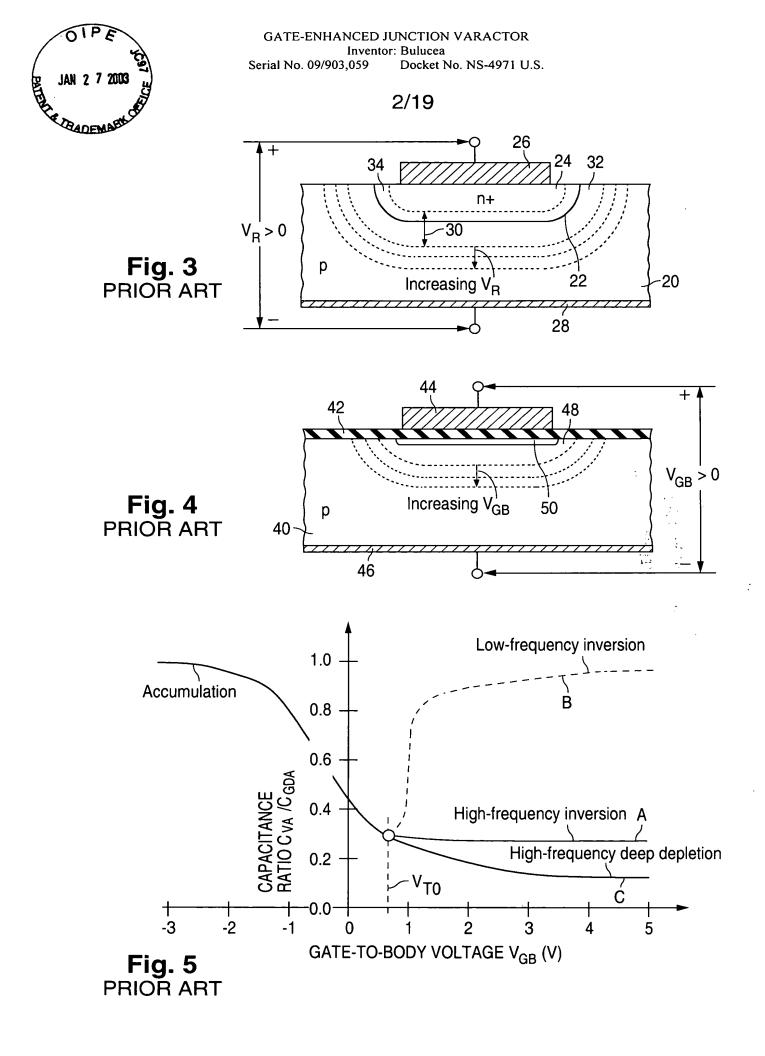


Fig. 2b PRIOR ART



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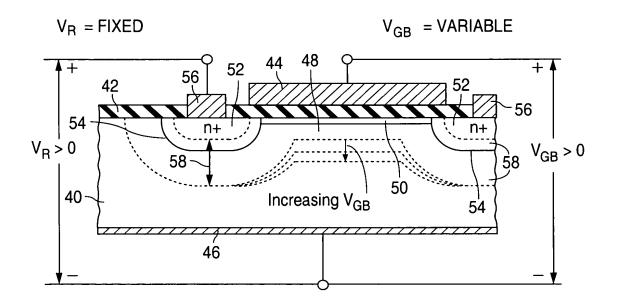


Fig. 6 PRIOR ART

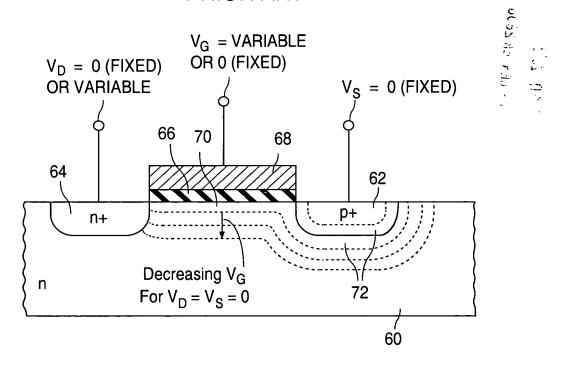
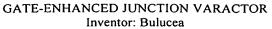
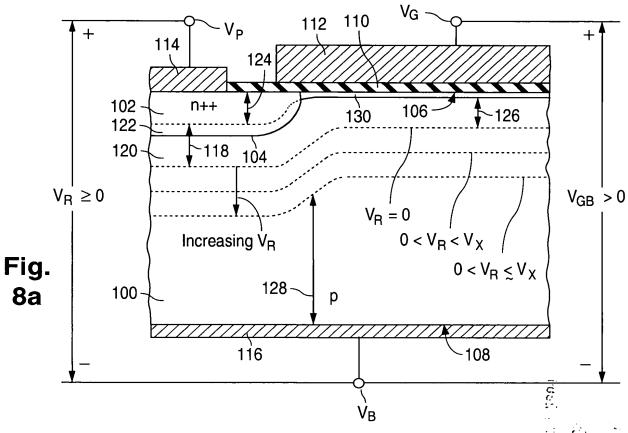


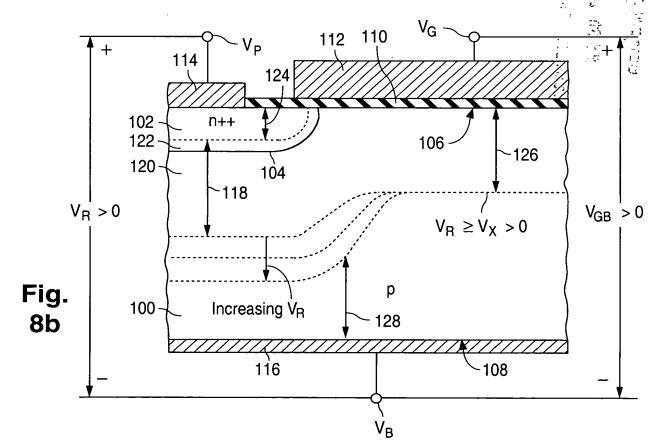
Fig. 7
PRIOR ART

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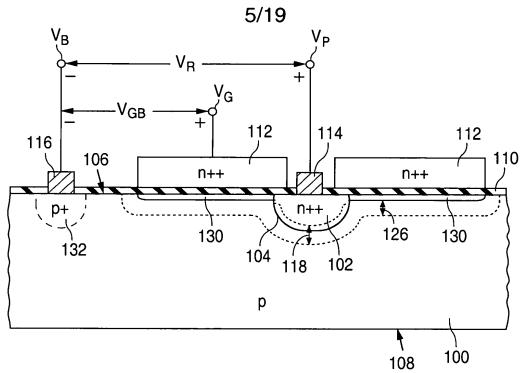


Fig. 9a

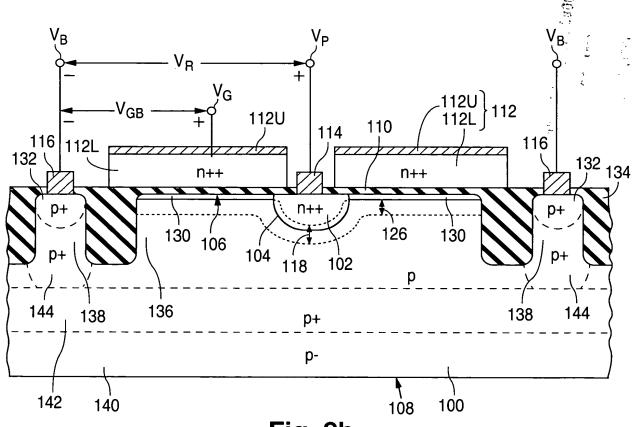
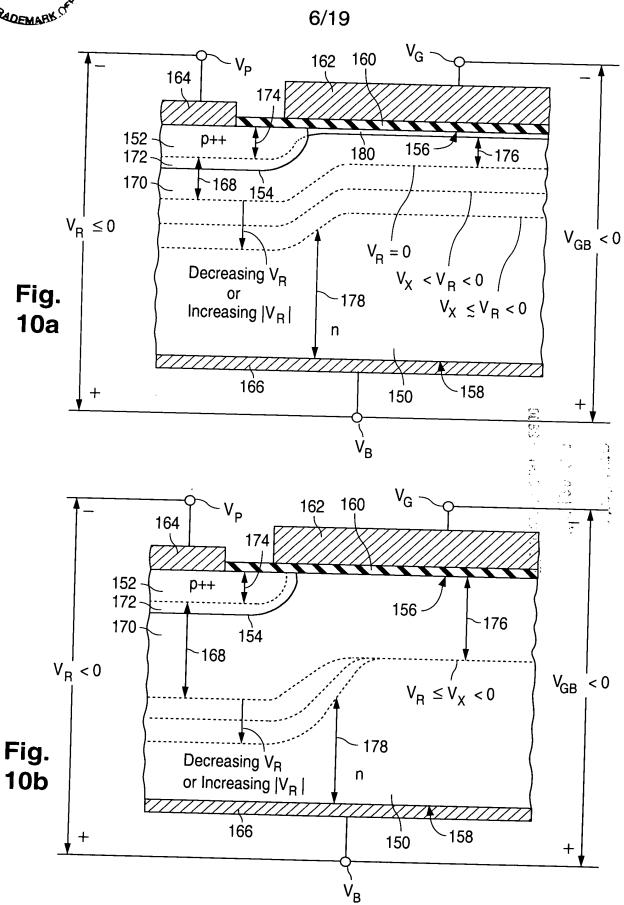


Fig. 9b



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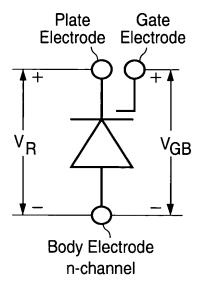


Fig. 11a

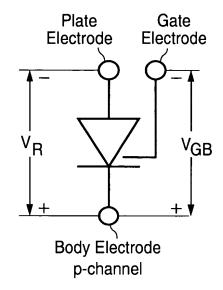


Fig. 11b

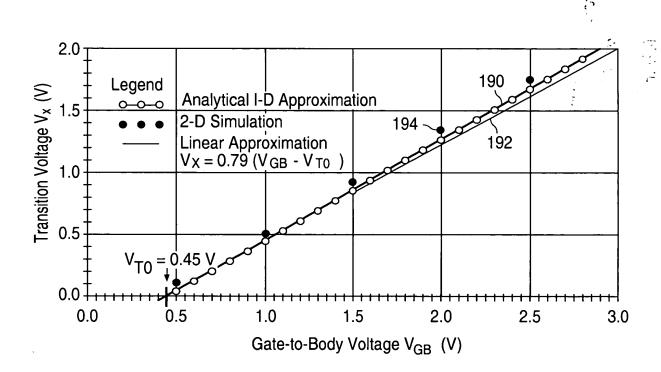


Fig. 12

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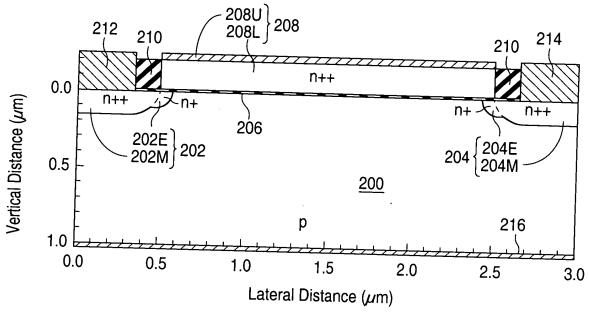
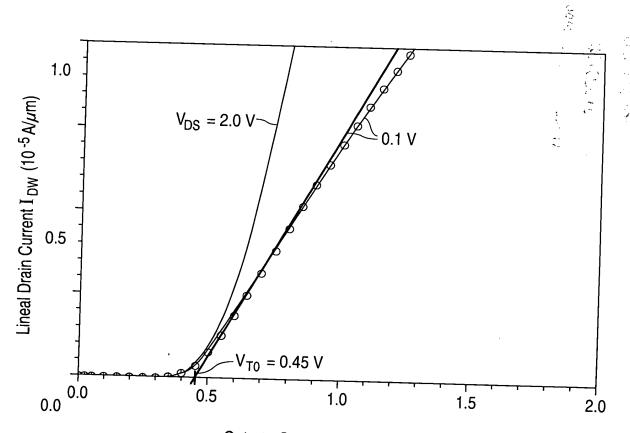


Fig. 13



Gate-to-Source Voltage V_{GS} (V)

Fig. 14

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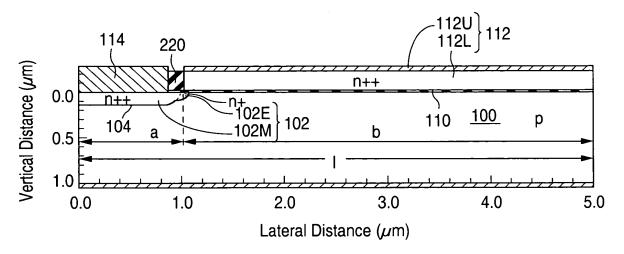


Fig. 15

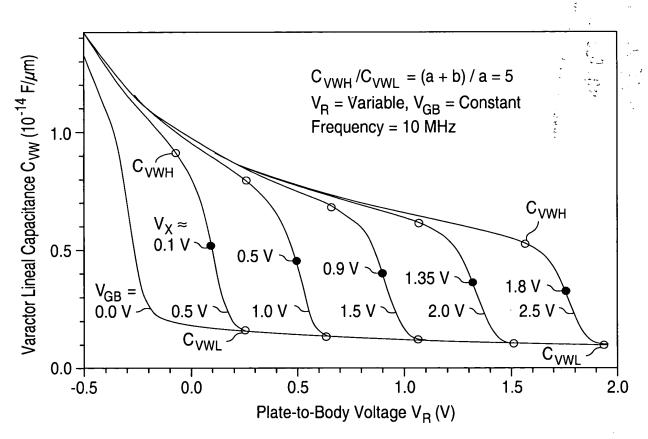


Fig. 16

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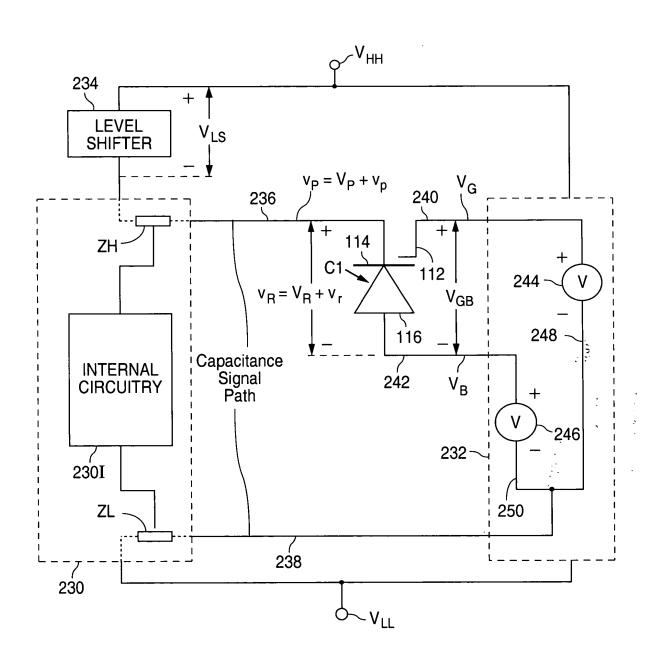


Fig. 17

OIPE (CS)

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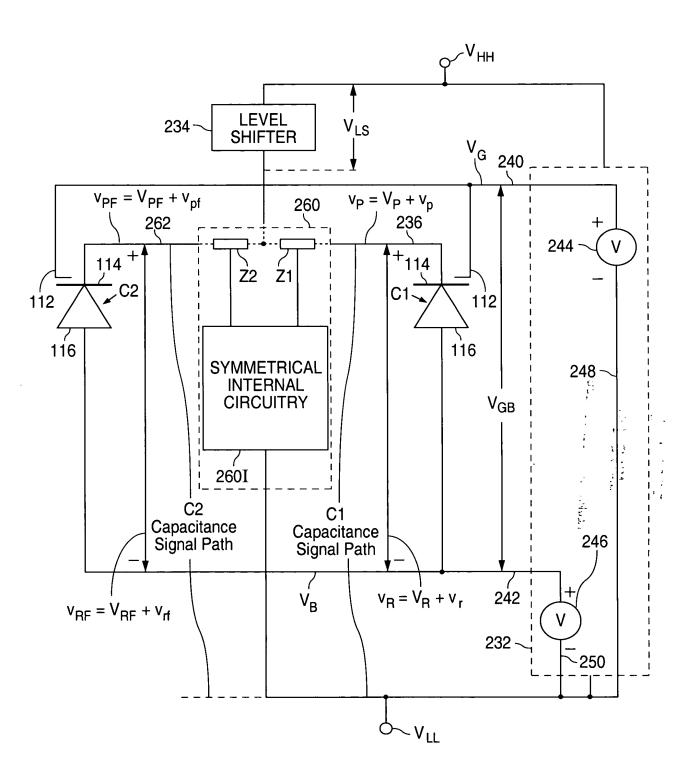
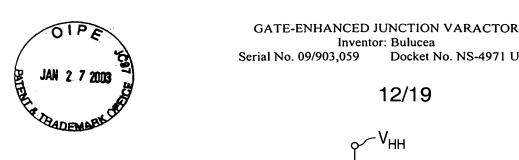
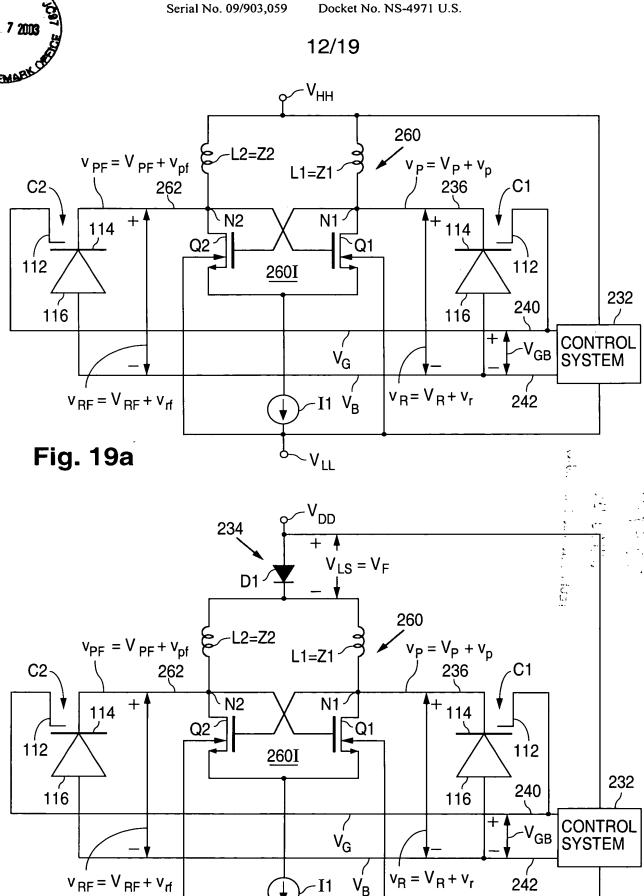


Fig. 18





 V_{LL}

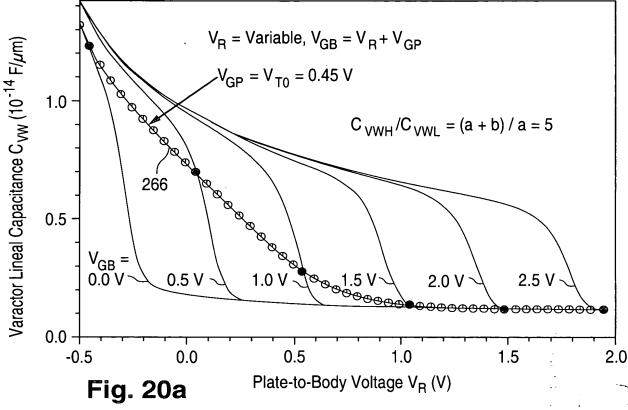
Fig. 19b

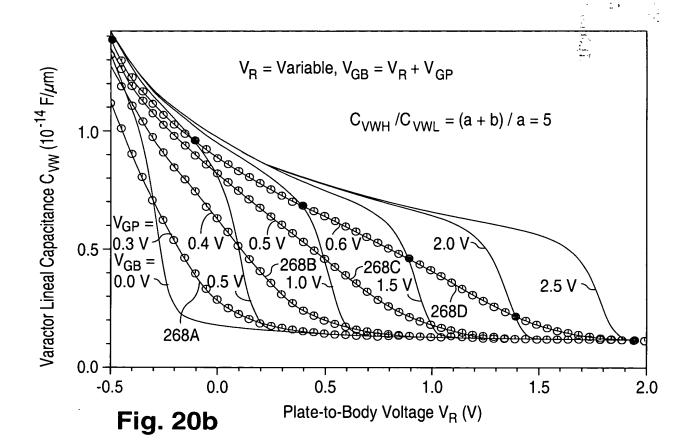


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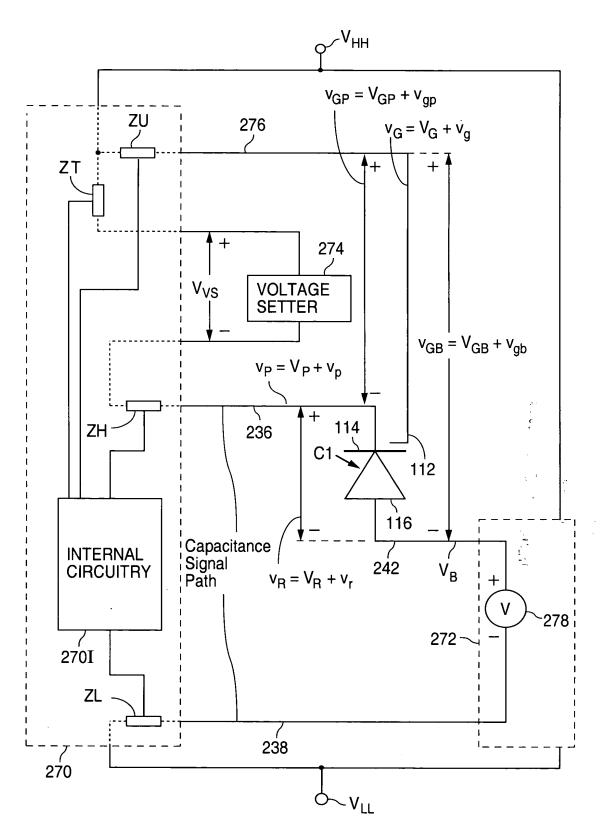


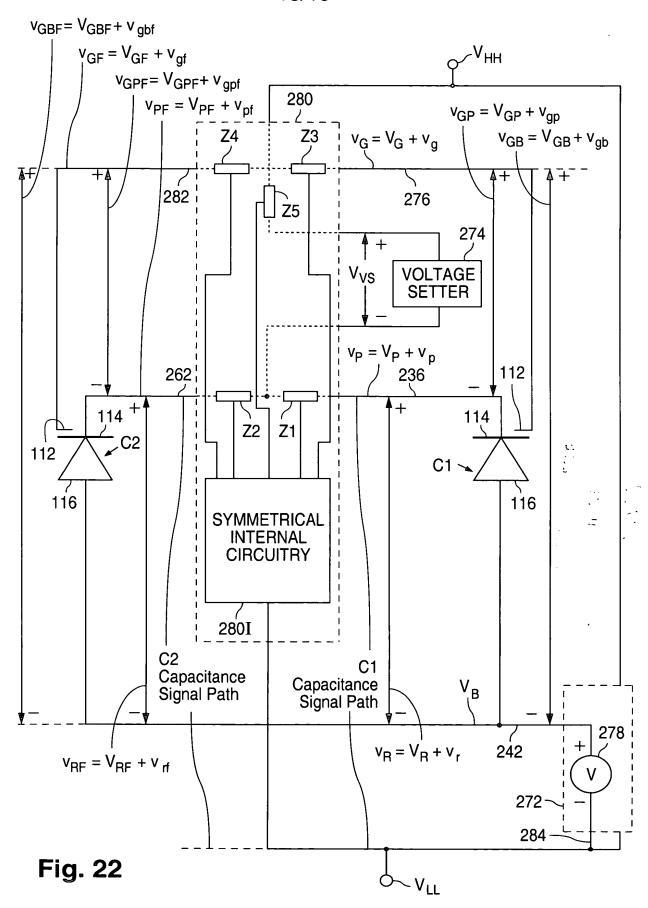
Fig. 21

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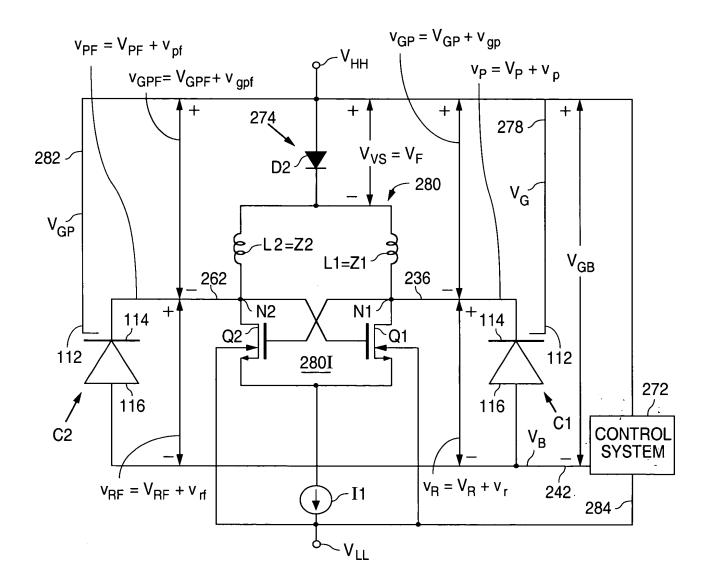


Fig. 23a



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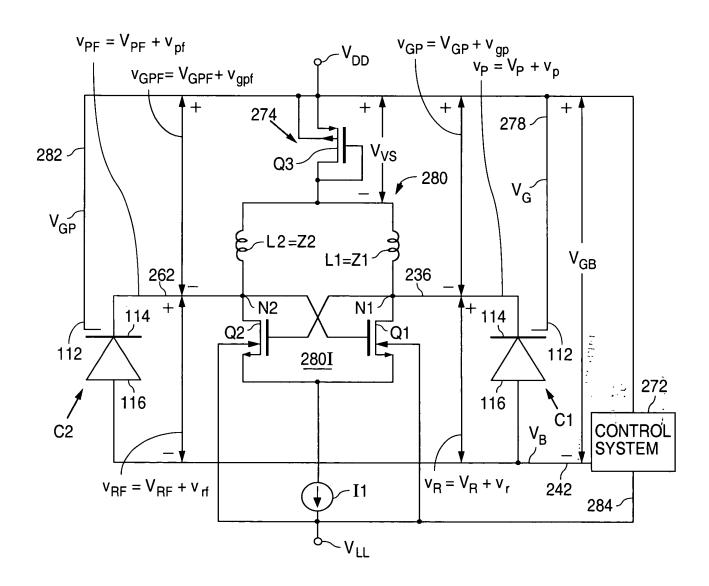


Fig. 23b



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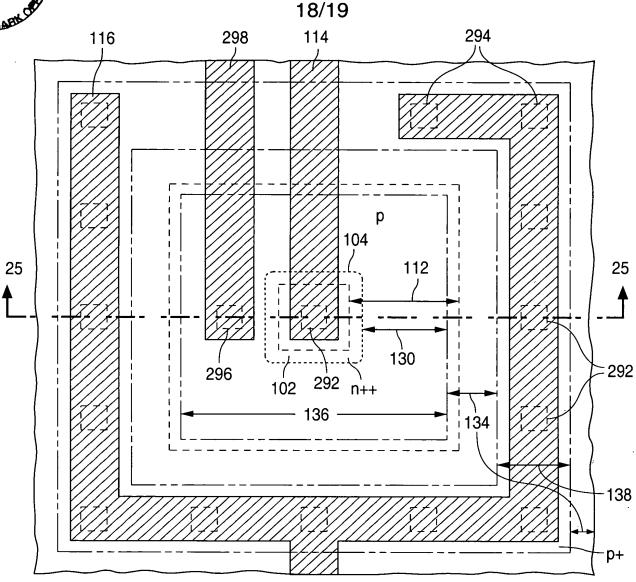
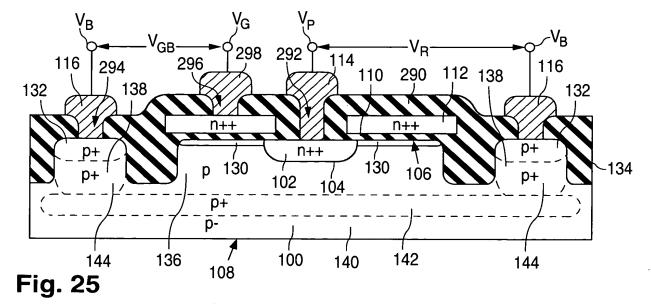


Fig. 24



GATE-ENHANCED JUNCTION VARACTOR

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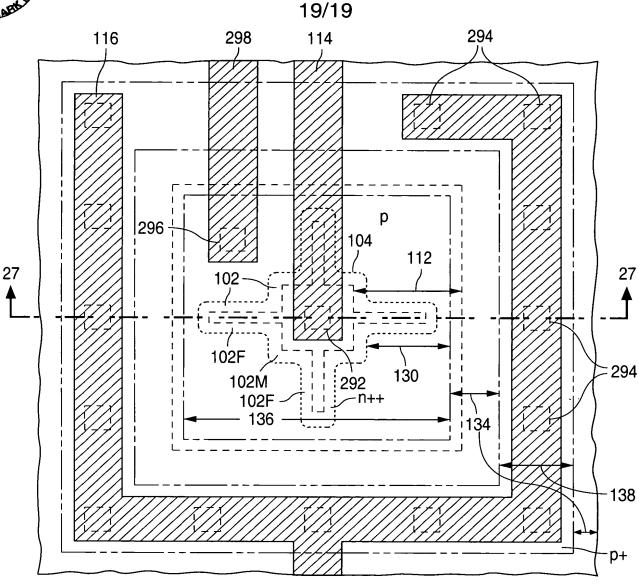


Fig. 26

